

501.43386X00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Y. WADA, et al  
Serial No.: (not yet assigned)  
Filed: January 30, 2004  
For: FABRICATION METHOD OF SEMICONDUCTOR  
INTEGRATED CIRCUIT DEVICE  
Group:

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

January 30, 2004

Sir:

The following preliminary amendments and remarks are respectfully  
submitted in connection with the above-identified application, as listed below and as  
set forth on the following pages:

Substitute Specification and Marked Up Version of Original Specification;

Amendment of the Abstract;

Amendment of the Claims; and

Remarks are included following the amendments.